

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : 7,219,201 Confirmation No. 1408
Issue Date : May 15, 2007
Applicant : N. KASAKO et al
Appl. No. : 10/748,886
Filed : December 30, 2003
Title : REMOTE STORAGE DISK CONTROL DEVICE AND METHOD
FOR CONTROLLING THE SAME
Docket No. : H-1215
Customer No.: 24956

REQUEST FOR CERTIFICATE OF CORRECTION
APPLICANTS' MISTAKE (37 CFR §1.323)
AND
PATENT OFFICE MISTAKE (37 CFR §1.322)

Office of Patent Publications
ATTN: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 4, 2011

Sir:

Further to our Request for Certificate of Correction filed on October 31, 2006, enclosed is a completed Certificate of Correction form. Please issue a Certificate of Correction for the underscored material.

Pursuant to MPEP §1481, it is submitted that the mistake is either:

- (1) of a clerical nature,
- (2) of a typographical nature, or
- (3) a mistake of minor character.

- (1) constitute new matter, or
- (2) require reexamination.

By /Shrinath Malur/
Shrinath Malur, Registration No. 34,663
(703) 684-1120

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 2

PATENT NO. : 7,219,201
APPLICATION NO.: 10/748,886
ISSUE DATE : May 15, 2007
INVENTOR(S): Naohisa Kasako, Shuji Kondo, Toru Suzuki

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the specification,

Column 4, lines 16-29, should read:

FIG. 2 shows a structure of a disk array device, which is described as an example of the first storage device 10 and the second storage device 20. Instead to of the disk array device, the first and second storage devices 10 and 20 may be any appropriate devices, such as, for example, semiconductor storage devices. For example, the disk array device 10 is equipped with various components including a channel control section 201, a remote communications interface 202, disk control sections 203, a shared memory 204, a cache memory 205, a switching control section 206 that is composed of cross bus switches that communicatively connect the components described above, a management terminal 207, and memory devices 208. The first and second storage devices 10 and 20 may have the same structure.

Mailing Address of Sender:

MATTINGLY & MALUR, PC
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 2 of 2

PATENT NO. : 7,219,201
APPLICATION NO.: 10/748,886
ISSUE DATE : May 15, 2007
INVENTOR(S): Naohisa Kasako, Shuji Kondo, Toru Suzuki

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the specification,

column 9, lines 29-55, should read:

Referring to FIG. 11, an example of a process flow in forming pairs will be described. In this example, it is assumed that the first storage device 10 is equipped with a third logical volume and a fifth logical volume, and the second storage device 20 is equipped with a fourth logical volume and a sixth logical volume. The information processing device 11 transmits a command to the first storage device 10 and the second storage device 20 for forming a pair of the third logical volume as being a primary volume 1101 and the fourth logical volume as being an auxiliary volume 1102, and a pair of the fifth logical volume as being a primary journal 1103 and the sixth logical volume as being an auxiliary journal 1104 ~~(1101, 1102)~~ (S1101, S1102). The pair management sections 704 of the first and second storage devices 10 and 20 store information indicating the states of the pairs in the pair management tables 1001 of the respective storage devices 10 and 20. The copy forming section 705 of the second storage device 20 transmits to the first storage device 10 a read request to read data in the primary volume; and upon receiving from the first storage device 10 a copy of the data in the primary volume, the second storage device 20 writes the data in the auxiliary volume (S1103). By this operation, the data in the primary volume and the data in the auxiliary volume can be matched with each other. A processing that brings the primary volume in conformity with the auxiliary volume by a pair forming instruction is called an "initial copy" processing.

Mailing Address of Sender:

MATTINGLY & MALUR, PC
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314